

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Harzanu et al. Att'y Docket: 2511/102


Serial No: Not Yet Assigned Art Unit: 2858

Date Filed: September 11, 2003 Examiner: Dole, T.

Invention: METHOD AND APPARATUS FOR THE ELECTRICAL TESTING OF PRINTED
CIRCUIT BOARDS EMPLOYING INTERMEDIATE LAYER GROUNDING

CERTIFICATE OF MAILING

I hereby certify that this document, along with any other papers referred to as being attached or enclosed, is being deposited with the United States Postal Service as **Express Mail** EV 333081172 US. with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 11, 2003.


Samuel J. Petuchowski

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Amendments to the Title and Specification begin on page 2 of this paper.

A Listing of Claims begins on page 3 of this paper.

Remarks/Arguments begin on page 4.